

	L #	Hits	Search Text	DBs
1	L1	896	257/686.ccls.	USP AT; US-P GPU B
2	L2	186	1 and (capacitor\$1 condenser\$1)	USP AT; US-P GPU B
3	L3	741	257/777.ccls.	USP AT; US-P GPU B
4	L4	170	3 and (capacitor\$1 condenser\$1)	USP AT; US-P GPU B
5	L5	118	2 not 4	USP AT; US-P GPU B
6	L6	31	5 and (processor\$1 microprocessor\$1)	USP AT; US-P GPU B
7	L7	87	5 not 6	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
8	L8	849	257/724.ccls.	USP AT; US-P GPU B
9	L9	299	8 and (capacitor\$1 condenser\$1)	USP AT; US-P GPU B
10	L10	258	9 not (2 4)	USP AT; US-P GPU B
11	L11	48	10 and (processor\$1 microprocessor\$1)	USP AT; US-P GPU B
12	L12	6	("4349862" "4574255" "5708570" "5874770" "6075285" "6153290").PN.	USP AT
13	L13	27	("3880493" "4322778" "4636918" "4754366" "4982311" "4994936" "5034850" "5049979" "5212402" "5258575" "5266821" "5307309" "5366931" "5472900" "5475565" "5508881" "5557502" "5583739" "5625221" "5633785" "5635741" "5668399" "5731960" "5739576" "5793076" "5856937" "5864177").PN.	USP AT

	L #	Hits	Search Text	DBs
14	L14	210	10 not 11	USP AT; US-P GPU B
15	L15	8	("4574255" "5502889" "5854534" "5953213" "6052287" "6137167" "6333857" "6362525").PN.	USP AT
16	L16	1659	(257/723,691).ccls.	USP AT; US-P GPU B
17	L17	470	16 and (capacitor\$1 condenser\$1)	USP AT; US-P GPU B
18	L18	287	17 not (2 4 10)	USP AT; US-P GPU B
19	L19	46	(2 5 10) and cpu\$1	USP AT; US-P GPU B
20	L20	28	19 not (6 11)	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
21	L21	79	18 and (processor\$1 microprocessor\$1 cpu\$1)	USP AT; US-P GPU B
22	L22	79	21 not 19	USP AT; US-P GPU B
23	L23	9	("5162977" "5483413" "5592391" "5912809" "6104258" "6215373" "6288906" "6346743" "6353540").PN.	USP AT
24	L24	208	18 not 22	USP AT; US-P GPU B
25	L25	3094	(361/760,782,783,767,7 68,792,793,794,795).cc ls.	USP AT; US-P GPU B
26	L26	837	25 and (capacitor\$1 condenser\$1)	USP AT; US-P GPU B
27	L27	734	26 not (2 4 10 18)	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
28	L28	133	27 and (processor\$1 microprocessor\$1 cpu\$1)	USP AT; US-P GPU B
29	L29	601	27 not 28	USP AT; US-P GPU B
30	L30	31	29 and ((controlled adj collapse) c4)	USP AT; US-P GPU B
31	L31	570	29 not 30	USP AT; US-P GPU B
32	L32	171	31 and (watt\$1 \$1w)	USP AT; US-P GPU B
33	L33	73	2 and (processor\$1 microprocessor\$1 cpu\$1)	USP AT; US-P GPU B
34	L34	97	(6 11 19 21 28) and (watt\$1 \$1w)	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
35	L35	18	(6 11 19 21 28) and ((watt\$1 \$1w) with power)	USP AT; US-P GPU B
36	L36	10	34 and watt\$1	USP AT; US-P GPU B
37	L37	399	31 not 32	USP AT; US-P GPU B
38	L38	52	37 and decoupling	USP AT; US-P GPU B
39	L39	347	37 not 38	USP AT; US-P GPU B
40	L40	775	257/700.ccls.	USP AT; US-P GPU B
41	L41	227	40 and (capacitor\$1 condenser\$1)	USP AT; US-P GPU B

	L #	Hits	Search Text	DBs
42	L42	111	41 not (2 4 10 18 27)	USP AT; US-P GPU B
43	L43	18	42 and (processor\$1 microprocessor\$1 cpu\$1)	USP AT; US-P GPU B
44	L44	93	42 not 43	USP AT; US-P GPU B
45	L45	0	43 and watt\$1	USP AT; US-P GPU B